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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Anthony X. Jarvis, et al.

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PROCESSORS AND METHOD OF OPERATION

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Examiner

(Not Yet Known)

Assistant Commissioner for Patents Washington, D.C. 20231

Sir:

## **CERTIFICATE OF MAILING**

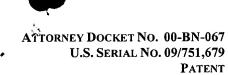
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